

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1326	382/239.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/07 13:01
L2	178	382/239.ccls. and truncat\$\$	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/07 13:01
L3	177	382/239.ccls. and truncat\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/07 13:01
L4	10	382/239.ccls. and truncat\$4 with parameter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/07 13:07
L5	1085	truncat\$4 with parameter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/07 13:07
L6	624	truncat\$4 with parameter and (encod\$4 or compress\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/07 13:08
L7	490	truncat\$4 with parameter and (encod\$4 or compress\$4) and (adapt\$4 or adjust\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/07 13:08

## EAST Search History

L8	35	truncat\$4 with parameter same (adjust\$4 or adpat\$5) and (encod\$4 or compress\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/07 13:09
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Relevance scale ☐ ☐ ☐ ☐ ☐**1** [Non-standard stringology: algorithms and complexity](#)

S. Muthukrishnan, Krishna Palem

May 1994 **Proceedings of the twenty-sixth annual ACM symposium on Theory of computing STOC '94**

Publisher: ACM Press

Full text available: pdf(1.11 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**2** [Innovative Applications: A dynamically reconfigurable adaptive viterbi decoder](#)

Sriram Swaminathan, Russell Tessier, Dennis Goeckel, Wayne Burleson

February 2002 **Proceedings of the 2002 ACM/SIGDA tenth international symposium on Field-programmable gate arrays FPGA '02**

Publisher: ACM Press

Full text available: pdf(235.26 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

The use of error-correcting codes has proven to be an effective way to overcome data corruption in digital communication channels. Although widely-used, the most popular communications decoding algorithm, the Viterbi algorithm, requires an exponential increase in hardware complexity to achieve greater decode accuracy. In this paper, we describe the analysis and implementation of a reduced-complexity decode approach, the adaptive Viterbi algorithm (AVA). Our AVA design is implemented in reconfigu ...

**Keywords:** FPGA, Viterbi coding, dynamic reconfiguration**3** [Low power motion estimation design using adaptive pixel truncation](#)

Zhong-Li He, Kai-Keung Chan, Chi-Ying Tsui, Ming L. Liou

August 1997 **Proceedings of the 1997 international symposium on Low power electronics and design ISLPED '97**

Publisher: ACM Press

Full text available: pdf(754.40 KB)

Additional Information: [full citation](#), [references](#), [citations](#)**4** [SAVE: an algorithm for smoothed adaptive video over explicit rate networks](#)

N. G. Duffield, K. K. Ramakrishnan, Amy R. Reibman

December 1998 **IEEE/ACM Transactions on Networking (TON)**, Volume 6 Issue 6